

**IN THE SPECIFICATION:**

Substitute the following paragraph for the pending paragraph beginning on page 16, line 17 (the third full paragraph on page 16).

Referring additionally now to FIG. 11, a schematic block diagram of an uplink receiver 112 of the MPP VDSL telemetry system 10 of the present invention is representatively illustrated. The receiver 112 may be used for the receiver 58 of the modem 32 in the telemetry system 10. The receiver 112 includes a conventional input amplifier and auto gain control (not shown) for boosting the level of the incoming uplink signal, which is the summation of the signals transmitted by the multiple individual downhole transmitters. Thus, in receiving the signals transmitted by the downhole transmitters, the data superframes transmitted from the downhole modems are summed. The receiver 112 also includes an analog to digital converter (ADC) and filter 114, a time domain equalizer (TDQ) 116, a cyclic prefix stripper 118, a discrete Fourier transform (DFT) demodulator 120, a frequency domain equalizer (FDQ) 122, a constellation decoder and bit extractor 124, a de-interleaver 126, a Reed-Solomon decoder 128, a descrambler 130, a cyclic redundancy checker 132 and a data deframer 134.